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J1017 U.S. PTO  
4931 U.S. PTO

03-04-02

J1017 U.S. PTO  
10/08/7712  
03/01/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
UTILITY PATENT APPLICATION TRANSMITTAL LETTER

To: Assistant Commissioner for Patents  
Box Patent Application  
Washington D.C., 20231

Dear Sir:

Re Attorney Docket No. ONS00030P, transmitted herewith is an application for Nonprovisional Utility Patent, entitled LOW VOLTAGE OUTPUT DRIVE CIRCUIT, for filing under 37 C.F.R. §1.53(b), with a Mailing Date of March 1, 2002 and Express Mail Label No.: EK617876067US, as a Continuation-In-Part (CIP) Patent Application of prior US Patent Application No. 09/580,560, filed on May 30, 2000, having U.S. Examiner Long T. Nguyen, in Group Art Unit 2816.

The inventors are Jefferson W. Hall and Jade H. Alberkrack.

Patent application filing papers include,

- ✓ 20 page(s) of specification and claims, and 4 sheets of formal, 3 sheets of informal drawings.
- ✓ Newly executed oath or declaration combined with power of attorney on 1 page(s).
- ✓ Copy of oath or declaration from prior U.S. application serial no. 09/580,560.
- ✓ The following named inventor(s) from the prior application are hereby deleted from this application in accordance with 37 C.F.R. §1.63(d)(2): Kazuo Ito.
- ✓ An assignment transmittal letter and assignment of the invention to Semiconductor Components Industries, LLC.
- ✓ A preliminary amendment.
- ✓ Return receipt postcard.

Incorporation by Reference (for Continuation/Division/CIP application). The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein. Since the present application is based on a prior US application, please amend the specification by adding the following sentence before the first sentence of the specification:

The present application is based on prior US application No. 09/580,560, filed on May 30, 2000, which is hereby incorporated by reference, and priority thereto for common subject matter is hereby claimed.

The filing fee for claims as filed, adjusted by additions or deletions made to claims by amendment if any, is calculated as follows:

Total Claims	16 - 20	x \$18 =	\$0.00
Independent Claims	3 - 3	x \$84 =	\$0.00
Basic Fee		=	<u>\$740.00</u>
Total Filing Fee		=	<u>\$740.00</u>

Please charge Deposit Account No. 501086 in the amount of \$740.00 for the Total Filing Fee, and the Extension Fee under 37 C.F.R. §1.136(a) if applicable. The Commissioner is hereby authorized to charge any additional fees which may be required now or in the future during the entire pendency of this application under 37 C.F.R. §1.16 or 37 C.F.R. §1.17, including any present or future time extension fees which may be required, or credit any overpayment to Deposit Account No. 501086. This transmittal letter has 2 total page(s).

Date

March 1, 2002

James J. Stipanuk

44,358  
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PATENT APPLICATION

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS: Jefferson W. Hall et al

CASE NO.: ONS00030 P

ENTITLED: LOW VOLTAGE OUTPUT DRIVE CIRCUIT

Semiconductor Components Industries

L.L.C.

Patent Administration - A700  
P. O. Box 62890  
Phoenix, Arizona 85082-2890

March 1, 2002

**CERTIFICATE OF EXPRESS MAILING**

COMMISSIONER OF PATENTS  
& TRADEMARKS  
BOX NEW PATENT APPLICATION  
Washington D.C. 20231

Sir:

I certify that the above-referenced continuation-in-part patent application was deposited, postage prepaid, as Express Mail having the mailing label number written below with the United States Postal Service addressed to:

COMMISSIONER OF PATENTS  
& TRADEMARKS  
Washington D.C. 20231

on March 1, 2002

By: Lydia McNamara  
Lydia McNamara

Date: March 1, 2002

Mailing Label No.: EK617876067US